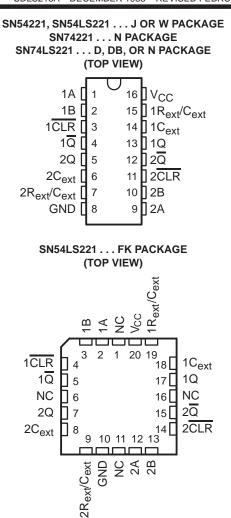
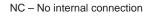
- Dual Versions of Highly Stable SN54121 and SN74121 One Shots
- SN54221 and SN74221 Demonstrate Electrical and Switching Characteristics That Are Virtually Identical to the SN54121 and SN74121 One Shots
- Pinout Is Identical to the SN54123, SN74123, SN54LS123, and SN74LS123
- Overriding Clear Terminates Output Pulse
- Package Options Include Plastic Small-Outline (D), Shrink Small-Outline (DB) Packages, Ceramic Chip Carriers (FK) and Flat Packs (W), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

ТҮРЕ	MAXIMUM OUTPUT PULSE LENGTH(S)
SN54221	21
SN74221	28
SN54LS221	49
SN74LS221	70



description

The '221 and 'LS221 devices are monolithic dual multivibrators with performance characteristics virtually identical to those of the '121 devices. Each multivibrator features a negative-transition-triggered input and a positive-transition-triggered input, either of which can be used as an inhibit input.



Pulse triggering occurs at a particular voltage level and is not directly related to the transition time of the input pulse. Schmitt-trigger input circuitry (TTL hysteresis) for B input allows jitter-free triggering from inputs with transition rates as slow as 1 V/s, providing the circuit with excellent noise immunity, typically of 1.2 V. A high immunity to V_{CC} noise, typically of 1.5 V, is also provided by internal latching circuitry.

Once fired, the outputs are independent of further transitions of the A and B inputs and are a function of the timing components, or the output pulses can be terminated by the overriding clear. Input pulses can be of any duration relative to the output pulse. Output pulse length can be varied from 35 ns to the maximums shown in the above table by choosing appropriate timing components. With $R_{ext} = 2 k\Omega$ and $C_{ext} = 0$, an output pulse typically of 30 ns is achieved, which can be used as a dc-triggered reset signal. Output rise and fall times are TTL compatible and independent of pulse length. Typical triggering and clearing sequences are shown as a part of the switching characteristics waveforms.

Pulse-width stability is achieved through internal compensation and is virtually independent of V_{CC} and temperature. In most applications, pulse stability is limited only by the accuracy of external timing components.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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description (continued)

Jitter-free operation is maintained over the full temperature and V $_{
m CC}$ ranges for more than six decades of timing capacitance (10 pF to 10 μ F) and more than one decade of timing resistance (2 k Ω to 30 k Ω for the SN54221, 2 k Ω to 40 k Ω for the SN74221, 2 k Ω to 70 k Ω for the SN54LS221, and 2 k Ω to 100 k Ω for the SN74LS221). Throughout these ranges, pulse width is defined by the relationship: $t_w(out) = C_{ext}R_{ext} \ln 2 \approx 0.7 C_{ext}R_{ext}$. In circuits where pulse cutoff is not critical, timing capacitance up to 1000 μ F and timing resistance as low as 1.4 k Ω can be used. Also, the range of jitter-free output pulse widths is extended if V_{CC} is held to 5 V and free-air temperature is 25°C. Duty cycles as high as 90% are achieved when using maximum recommended R_T . Higher duty cycles are available if a certain amount of pulse-width jitter is allowed.

The variance in output pulse width from device to device typically is less than ±0.5% for given external timing components. An example of this distribution for the '221 is shown in Figure 3. Variations in output pulse width versus supply voltage and temperature for the '221 are shown in Figures 4 and 5, respectively.

Pin assignments for these devices are identical to those of the SN54123/SN74123 or SN54LS123/SN74LS123 so that the '221 or 'LS221 devices can be substituted for those products in systems not using the retrigger by merely changing the value of Rext and/or Cext; however, the polarity of the capacitor must be changed.

The SN54221 and SN54LS221 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74221 and SN74LS221 are characterized for operation from 0°C to 70°C.

	NPUTS		OUTPUTS							
CLR	Α	В	Q	Q						
L	Х	Х	L	Н						
Х	Н	Х	L	Н						
Х	Х	L	L	Н						
н	L	\uparrow	†	പ്						
н	\downarrow	Н	†	പ						
 +	L	Н	л [†]	<u></u>						

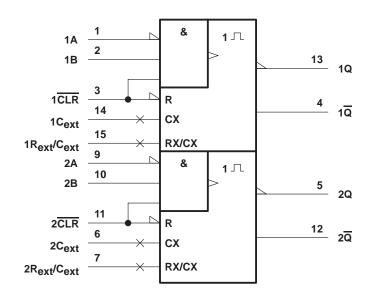
FUNCTION TABLE	
(each monostable multivil	brator)

[†]Pulsed-output patterns are tested during AC switching at 25°C with $R_{ext} = 2 k\Omega$, and C_{ext} = 80 pF.

[‡]This condition is true only if the output of the latch formed by the two NAND gates has been conditioned to the logic 1 state prior to CLR going high. This latch is conditioned by taking either A high or B low while CLR is inactive (high).

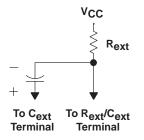


logic symbol[†]



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, DB, J, N, and W packages.

timing component connections

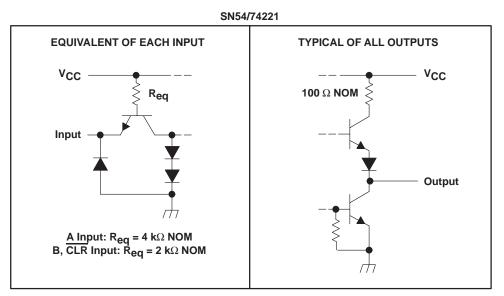


NOTE: Due to the internal circuit, the Rext/Cext terminal is never more positive than the Cext terminal.

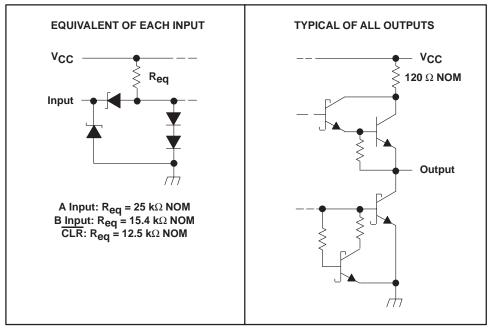


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schematics of inputs and outputs



SN54/74LS221





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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V _{CC} Input voltage range, V _I (see Note 1): 'LS221 .		
Package thermal impedance, θ_{JA} (see Note 2):	: D package	113°C/W
	DB package	131°C/W
	N package	
Storage temperature range, T _{stg}		–65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. The package thermal impedance is calculated in accordance with JESD 51, except for through-hole packages, which use a trace length of zero

recommended operating conditions

				SN54221		5	SN74221		UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage		4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	A input	2			2			V
VIL	Low-level input voltage	A input			0.8			0.8	V
ЮН	High-level output current				-800			-800	μΑ
IOL	Low-level output current				16			16	mA
Δv/Δt	Disc or fall of input pulse rate	B input	1*			1			V/s
	Rise or fall of input pulse rate	A input	1*			1			V/µs
Т _А	Operating free-air temperature		-55		125	0		70	°C

* On products compliant to MIL-PRF-38535, this parameter is not production tested.



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER		NDITIONO†		SN54221		ę	SN74221		UNIT
	PARAMETER	TEST CC	NDITIONS [†]	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
V _{T+}	Positive-going threshold voltage, B input	V _{CC} = MIN			1.55	2*		1.55	2	V
V _T -	Negative-going threshold voltage, B input	V _{CC} = MIN		0.8*	1.35		0.8	1.35		V
۷IK	-	V _{CC} = MIN,	I _I = -12 mA			-1.5			-1.5	V
VOH		$V_{CC} = MIN,$	I _{OH} = -800 μA	2.4	3.4		2.4	3.4		V
VOL		$V_{CC} = MIN,$	I _{OL} = 16 mA		0.2	0.4		0.2	0.4	V
Ц	_	$V_{CC} = MAX,$	VI = 5.5 V			1			1	mA
	A input		$V_1 = 2.4 V$			40			40	
Чн	CLR, B input	V _{CC} = MAX,	v = 2.4 v			80			80	μA
	A input	V _{CC} = MAX,	$\lambda = 0.4 \lambda$			-1.6			-1.6	mA
¹IL	CLR, B input	VCC = MAX,	v] = 0.4 v			-3.2			-3.2	ША
los§		$V_{CC} = MAX$		-20		-55	-18		-55	mA
1	Quiescent				26	50*		26	50	
lcc	Triggered	V _{CC} = MAX			46	80*		46	80	mA

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

§ Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second.

timing requirements over recommended ranges of supply voltage and operating free-air temperature

			SN54	4221	SN74	221	UNIT
			MIN	MAX	MIN	MAX	UNIT
+	Pulse duration	A or B input	50		50		ns
tw	ruse duration	CLR	20		20		115
t _{su}	Setup time, inactive-state¶	CLR	15		15		ns
R _{ext}	External timing resistance		1.4*	30*	1.4	40	kΩ
C _{ext}	External timing capacitance		0*	1000*	0	1000	μF
	Output duty cycle	$R_{ext} = 2 k\Omega$		67%		67%	
		R _{ext} = MAX R _{ext}		90%		90%	

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

¶ Inactive-state setup time is also referred to as recovery time.



SN54221, SN54LS221, SN74221, SN74LS221 DUAL MONOSTABLE MULTIVIBRATORS WITH SCHMITT-TRIGGER INPUTS SDLS213A – DECEMBER 1983 – REVISED FEBRUARY 1998

switching characteristics V_{CC} = 5 V, R_L = 400 $\Omega,$ T_A = 25°C (see Figures 1 and 2)

PARAMETER	FROM	то	TEST CO	NDITIONS	s	N54221		S	N74221		UNIT						
PARAMETER	(INPUT)	(OUTPUT)	TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNIT						
ta	А	0				45	70		45	70							
^t PLH	В	Q			35	55		35	55								
	А	Q	C _{ext} = 80 pF,	$C_{ext} = 80 \text{ pF},$	$C_{ext} = 80 \text{ pr},$	$C_{ext} = 80 \text{pr},$	$C_{ext} = 00 \text{ pr},$	$C_{ext} = 00 \text{ pr},$	$C_{ext} = 00 \text{ pr},$	$R_{ext} = 2 k\Omega$		50	80		50	80	ns
^t PHL	В	Q				40	65		40	65							
^t PHL	CLR	Q	0 00 - 5				27			27							
^t PLH	ULK	Q	C _{ext} = 80 pF,	$R_{ext} = 2 k\Omega$			40			40	ns						
			C _{ext} = 80 pF,	$R_{ext} = 2 k\Omega$	70	110	150	70	110	150							
+	A or B	Q or Q	$C_{ext} = 0,$	$R_{ext} = 2 k\Omega$	17	30	50	17	30	50	ns						
t _w	AUD		C _{ext} = 100 pF,	$R_{ext} = 10 \ k\Omega$	650	700	750	650	700	750							
			$C_{ext} = 1 \ \mu F$,	$R_{ext} = 10 \ k\Omega$	6.5*	7	7.5*	6.5	7	7.5	ms						

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

recommended operating conditions

			SI	N54LS22	21	SI	N74LS22	1	UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage		4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	A input	2			2			V
VIL	Low-level input voltage	A input			0.7			0.8	V
ЮН	High-level output current				-400			-400	μΑ
IOL	Low-level output current				4			8	mA
Δv/Δt	Dise of fall of input pulse rate	B input	1*			1			V/s
	Rise or fall of input pulse rate	A input	1*			1			V/µs
Т _А	Operating free-air temperature		-55		125	0		70	°C

* On products compliant to MIL-PRF-38535, this parameter is not production tested.



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER			S	N54LS22	1	SI	N74LS22	1	UNIT
	FARAMETER	TEST CC	ONDITIONS [†]	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
V _{T+}	Positive-going threshold voltage, B input	V _{CC} = MIN			1	2*		1	2	V
V _{T-}	Negative-going threshold voltage, B input	V _{CC} = MIN		0.7*	0.9		0.8	0.9		V
VIK		$V_{CC} = MIN,$	I _I = -18 mA			-1.5			-1.5	V
Vон		$V_{CC} = MIN,$	I _{OH} = -400 μA	2.5	3.4		2.7	3.4		V
Vei		V _{CC} = MIN	$I_{OL} = 4 \text{ mA}$		0.25	0.4		0.25	0.4	V
VOL		ACC = MIM	IOT = 8 wy					0.35	0.5	v
Ц		$V_{CC} = MAX,$	VI = 7 V			0.1			0.1	mA
Чн		$V_{CC} = MAX,$	VI = 2.7 V			20			20	μΑ
	A input					-0.4			-0.4	
μL	CLR, B input	$V_{CC} = MAX,$	V _I = 0.4 V			-0.8			-0.8	mA
IOS§		$V_{CC} = MAX$		-20		-100	-20		-100	mA
	Quiescent				4.7	11		4.7	11	A
ICC	Triggered	V _{CC} = MAX			19	27*		19	27	mA

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

§ Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second.

timing requirements over recommended ranges of supply voltage and operating free-air temperature

			SN54L	S221	SN74L	.S221	UNIT
			MIN	MAX	MIN	MAX	UNIT
+	Pulse duration	A or B	50		50		ns
tw	ruse duration	CLR	40		40		115
t _{su}	Setup time, inactive state¶	CLR	15		15		ns
R _{ext}	External timing resistance		1.4*	70*	1.4	100	kΩ
C _{ext}	External timing capacitance		0*	1000*	0	1000	μF
	Output duty cycle	R _T = 2 kΩ		50%		50%	
		RT = MAX Rext		90%		90%	

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

 \P Inactive-state setup time is also referred to as recovery time.



SN54221, SN54LS221, SN74221, SN74LS221 DUAL MONOSTABLE MULTIVIBRATORS WITH SCHMITT-TRIGGER INPUTS SDLS213A – DECEMBER 1983 – REVISED FEBRUARY 1998

PARAMETER	FROM	то	TEST CO	NDITIONS	SN	54LS22	1	SN	74LS22	1	UNIT				
PARAMETER	(INPUT)	(OUTPUT)	TEST CO	TEST CONDITIONS		TYP	MAX	MIN	TYP	MAX	UNIT				
t=	А	Q				45	70		45	70					
^t PLH	В		0 00 - 5			35	55		35	55	ns				
t=	А		Cext = 60 pr,	C _{ext} = 80 pF,	$C_{ext} = 60 \text{ pr},$	$C_{ext} = 80 \text{ pr},$	Cext = 00 pr,	$R_{ext} = 2 K \Omega \Omega$		50	80		50	80	115
^t PHL	В	Q		[40	65		40	65					
^t PHL	CLR	Q	0 00 - 5			35	55		35	55	ns				
^t PLH	ULK	Q	C _{ext} = 80 pF,	$R_{ext} = 2 R_{22}$		44	65		44	65	115				
			C _{ext} = 80 pF,	$R_{ext} = 2 k\Omega$	70	120	150	70	120	150					
+	A or B	Q or \overline{Q}	$C_{ext} = 0,$	$R_{ext} = 2 k\Omega$	20	47	70	20	47	70	ns				
t _w	AUID		C _{ext} = 100 pF,	$R_{ext} = 10 \ k\Omega$	670	740	810	670	740	810					
			$C_{ext} = 1 \ \mu F$,	$R_{ext} = 10 \ k\Omega$	6*	6.9	7.5*	6	6.9	7.5	ms				

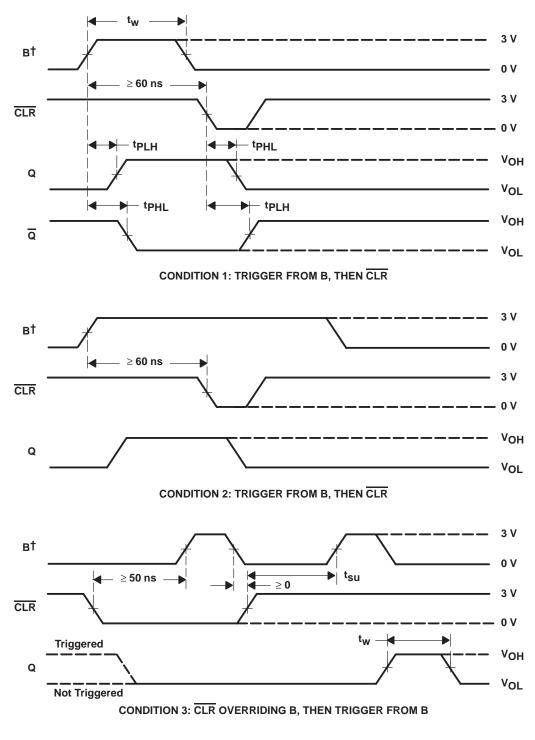
switching characteristics V_{CC} = 5 V, R_L = 2 k $\Omega,$ T_A = 25 $^{\circ}\text{C}$ (see Figures 1 and 2)

* On products compliant to MIL-PRF-38535, this parameter is not production tested.



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PARAMETER MEASUREMENT INFORMATION



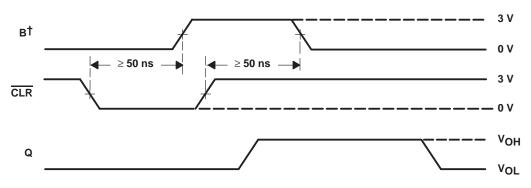
[†] A is low.

Figure 1. Switching Characteristics

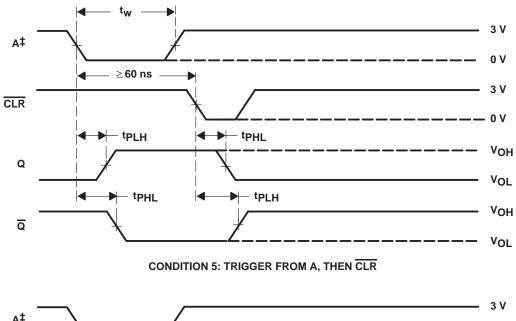


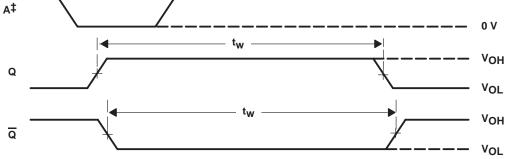
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PARAMETER MEASUREMENT INFORMATION



CONDITION 4: TRIGGERING FROM POSITIVE TRANSITION OF CLR





CONDITION 6: TRIGGER FROM A

[†] A is low.

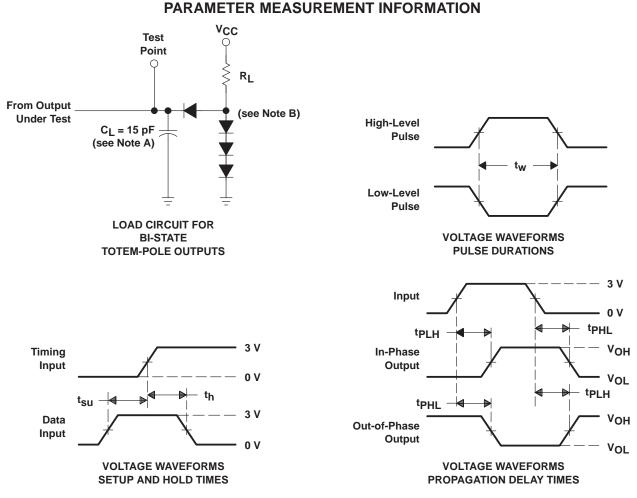
 \ddagger B and $\overline{\text{CLR}}$ are high.

- NOTES: A. Input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, Z_O \approx 50 Ω ; for SN54/74221, t_f \leq 7 ns, $t_f \le 7$ ns, for SN54/74LS221, $t_r \le 15$ ns, $t_f \le 6$ ns.
 - B. All measurements are made between the 1.5-V points of the indicated transitions for the SN54/74221 or between the 1.3-V points for the SN54/74LS221.





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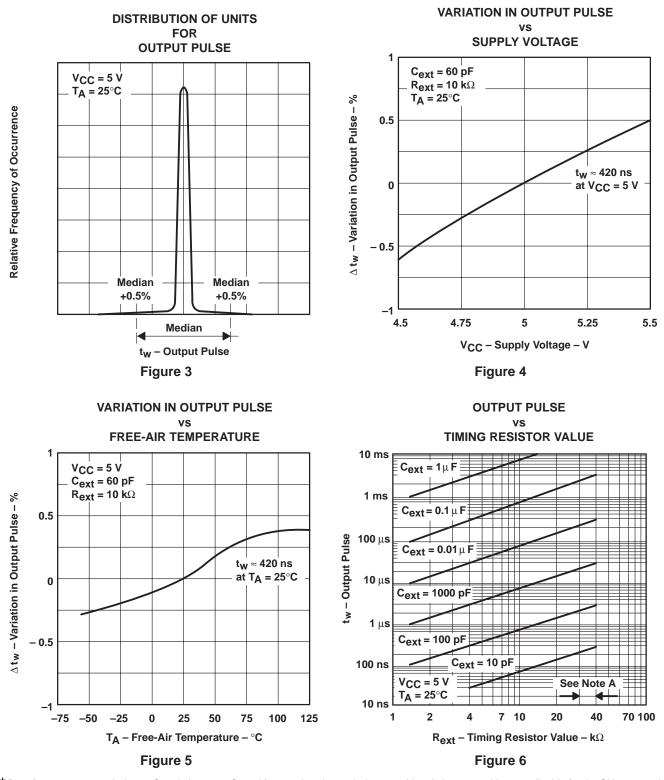
- NOTES: A. CL includes probe and jig capacitance.
 - B. All diodes are 1N3064 or equivalent.
 - C. In the examples above, the phase relationships between inputs and outputs have been chosen arbitrarily.
 - D. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, Z_O \approx 50 Ω and, for SN54/74221, t_f \leq 7 ns, t_f \leq 7 ns, for SN54/74LS221, t_f \leq 15 ns, t_f \leq 6 ns.
 - E. All measurements are made between the 1.5-V points of the indicated transitions for the SN54/74221 or between the 1.3-V points for the SN54/74LS221.

Figure 2. Load Circuits and Voltage Waveforms



SN54221, SN54LS221, SN74221, SN74LS221 DUAL MONOSTABLE MULTIVIBRATORS WITH SCHMITT-TRIGGER INPUTS SDLS213A – DECEMBER 1983 – REVISED FEBRUARY 1998

TYPICAL CHARACTERISTICS (SN54/74221 ONLY)[†]



[†] Data for temperatures below 0°C and above 70°C, and for supply voltages below 4.75 V and above 5.25 V are applicable for the SN54221 only. NOTE A: These values of resistance exceed the maximum recommended for use over the full military temperature range of the SN54221.



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